

HV-EPICS Test Station Status Report
August 7, 2019

DSG Staff: Pablo Campero, Peter Bonneau

1. To test two CAEN SY4527 system simultaneously, modified network configurations, CAEN service name and developed CSS-BOY screens.

- 1.1. Verified gateway, host name, and IP address for each CAEN-SY4527 system.
- 1.2. Modified service name for both system mainframes:

CAEN Mainframe number	IP address	Service name
Crate #1	129.57.86.37	hvcaentest1
Crate #2	129.57.86.124	hvcaentes2

- 1.3. Changed prefix names for all PVs used in the CSS screens. Modified from generic SY4527 to new service name (hvcaentest1 or hvcaentes2) for each crate.
- 1.4. Modified Java scripts used to set all channels on/off and main parameters simultaneously.

2. Tested three HV boards model: A7030TN on CAEN mainframe hvcaentest1

2.1. Set all channel for the three boards at values shown in the table below:

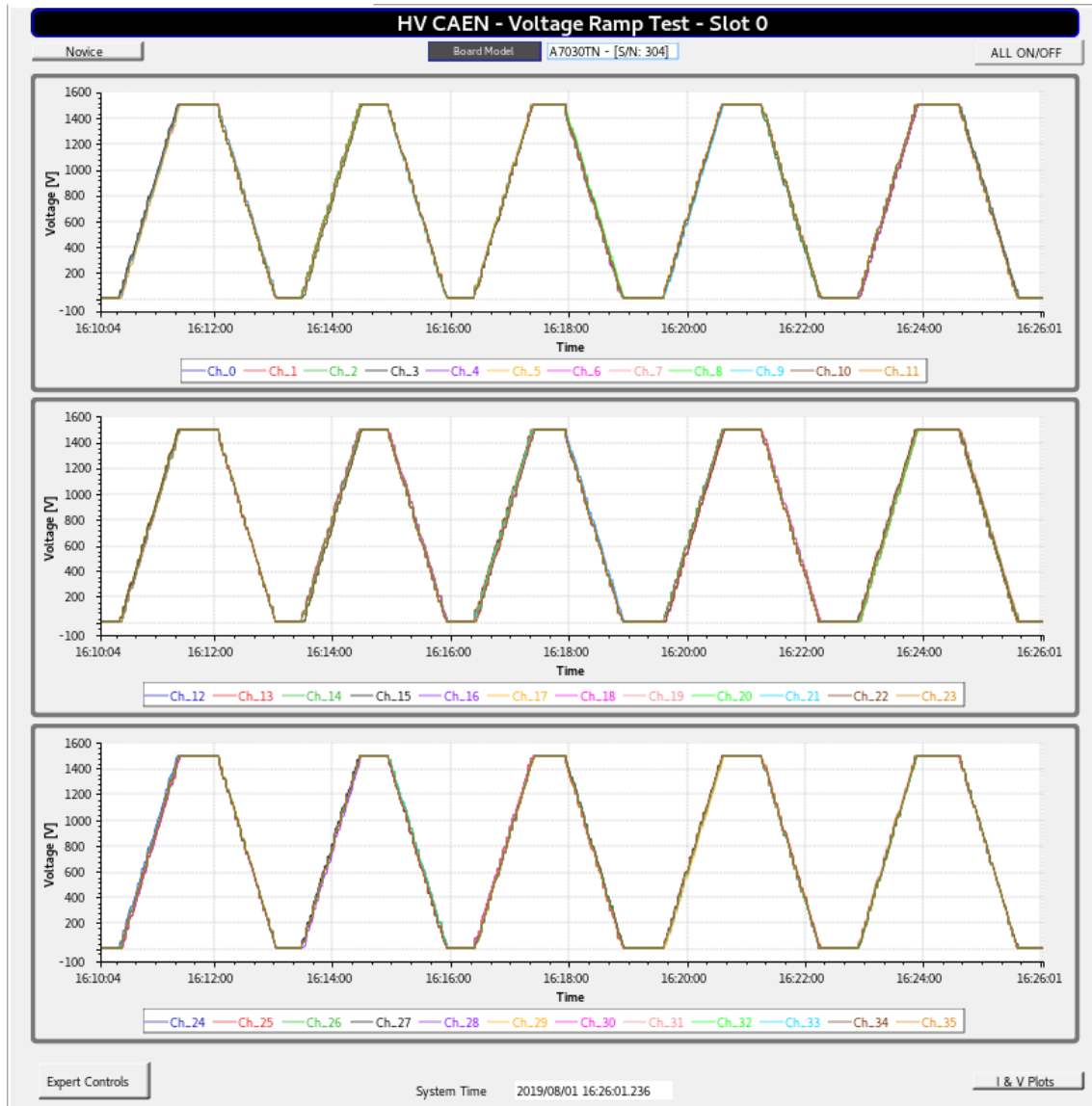
Board Model	7030TN	Set Voltage: 1500 V	
Serial Numbers	304, 297, 324	Ramp Up/Down Rate : 25 V/s	
Total Test per board	3	Load: 0 Ω	Imon: 0 μ A
Total # Voltage Ramp Up/Down per board	15/15	Vmax: 1800 V	Iset: 1000 μ A

- 2.2. No issues found, all channels ramped to the set voltage.
- 2.3. Concluded that, most probably hvcaentest2 mainframe is failing since same HV boards were tested in hvcaentes1 mainframe and none of them failed.

3. Generated spreadsheet with the details of the test performed for three HV CAEN A7030TN boards.

4. Added “Voltage Ramp Up/Down Test” CSS-BOY screen for HV-CAEN A7030TN to drop down menu in SY4527 Main screen to allow navigation between screens.

HV-EPICS Test Station Status Report August 7, 2019



HV-CAEN-A7030TN Voltage Ramp Up/Down Test CSS-BOY screen shows plots for voltage ramp up/down from HV board S/N 304

5. Generated CAEN-SY4527 Test Results Power Point presentation